

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	2	((augsborg-victor\$) and (bridges-jeff\$) and (mcilvaine-michael\$) and (sartorius-thomas\$) and (smith-rodney\$)).in.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/28 21:49
S1	1	interrupt\$2 near4 (co?process\$3) near4 (pseudo?instruction\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/04/04 15:45
S2	1	(interrupt\$2 near4 (co?process\$3)) with (pseudo?instruction\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/04/04 15:44
S3	1	(interrupt\$2 near4 (co?process\$3)) same (pseudo?instruction\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/04/04 15:44
S4	1	(interrupt\$2 near4 (co?process\$3)) and (pseudo?instruction\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/04/04 15:44
S5	5	interrupt\$2 near4 (co?process\$3) near4 register\$1	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/04/04 16:48
S6	4876	(interrupt\$1 near4 (control\$4 or arbit\$3 or rout\$3 or APIC)) same (interrupt\$1 near4 register\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/04/04 16:50
S7	606	(interrupt\$1 near4 (control\$4 or arbit\$3 or rout\$3 or APIC)) same (interrupt\$1 adj1 register\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/04/04 16:50
S8	297	(712/214).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/04/28 11:45
S9	487	(712/215).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/04/28 11:45
S10	56	((speculat\$5 or predict\$5) near4 issu\$3) with (pipelin\$3 near4 stag\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/04/28 11:55

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S11	2	((speculat\$5 or predict\$5) near4 result\$1) with (pipelin\$3 near4 stag\$3) with (availab\$5 or ready or finish\$3 or complet\$3 or done)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/04/28 11:57
S12	512	(712/218).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/04/28 11:57
S13	5	((speculat\$5 or predict\$5) near4 result\$1) with (forward\$3 or bypass\$3) with (pipelin\$3 near4 stag\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/04/28 11:59
S14	29453	((speculat\$5 or predict\$5) near4 result\$1) same ((forward\$3 or bypass\$3) with (pipelin\$3 near4 stag\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/04/28 11:59
S15	17	((speculat\$5 or predict\$5) near4 result\$1) same ((forward\$3 or bypass\$3) with (pipelin\$3 near4 stag\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/04/28 11:59
S16	528	(712/218).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/21 13:20
S17	300	(712/214).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/06/23 14:47
S18	494	(712/215).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/21 13:20
S19	15	confidence near4 valu\$2 near4 issu\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/06/23 14:48
S20	1	interrupt\$2 near4 (co?process\$3) near4 (pseudo?instruction\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/06/23 14:49
S21	1	(interrupt\$2 near4 (co?process\$3)) with (pseudo?instruction\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/06/23 14:49
S22	1	(interrupt\$2 near4 (co?process\$3)) same (pseudo?instruction\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/06/23 14:49

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S23	1	(interrupt\$2 near4 (co?process\$3)) and (pseudo?instruction\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/06/23 14:49
S24	5	interrupt\$2 near4 (co?process\$3) near4 register\$1	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/06/23 14:49
S25	2	((speculat\$5 or predict\$5) near4 result\$1) with (pipelin\$3 near4 stag\$3) with (availab\$5 or ready or finish\$3 or complet\$3 or done)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/06/23 14:49
S26	5	((speculat\$5 or predict\$5) near4 result\$1) with (forward\$3 or bypass\$3) with (pipelin\$3 near4 stag\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/06/23 14:50
S27	17	((speculat\$5 or predict\$5) near4 result\$1) same ((forward\$3 or bypass\$3) with (pipelin\$3 near4 stag\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/06/23 14:50
S28	7	(("5872947") or ("5964867") or ("5923862") or ("6360315") or ("6144982") or ("6393550") or ("5958041")).PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/06/23 14:54
S29	557	(712/218).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/12/06 11:58
S30	329	(712/214).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/21 13:20
S31	515	(712/215).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/12/06 11:59
S32	1	(predict\$3 near4 operand\$1 near4 (availab\$5 or read\$5 or free or result\$3)) with (pipeline near4 stag\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/12/06 12:01
S33	10	(predict\$3 with operand\$1 with (availab\$5 or read\$5 or free or result\$3)) with (pipeline near4 stag\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/12/06 12:03
S34	16	(predict\$3 with operand\$1 with (availab\$5 or read\$5 or free or result\$3)) same (pipeline near4 stag\$3)	US-PGPUB; USPAT	OR	OFF	2006/12/06 12:06

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S35	17	(predict\$3 with operand\$1 with (availab\$5 or read\$5 or free or result\$3)) same (pipeline with stag\$3)	US-PGPUB; USPAT	OR	OFF	2006/12/06 12:07
S36	4	(predict\$3 same operand\$1 same (availab\$5 or read\$5 or free or result\$3)) same (pipeline same stag\$3)	EPO; JPO; IBM_TDB	OR	OFF	2006/12/06 12:09
S37	114	(predict\$3 same operand\$1 same (availab\$5 or read\$5 or free or result\$3)) same (pipeline same stag\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/12/06 12:09
S38	3	((predict\$3 with operand\$1) near4 (availab\$5 or read\$5 or free or result\$3)) same (pipeline same stag\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/12/06 12:11
S39	7	((predict\$3 near4 operand\$1) with (availab\$5 or read\$5 or free or result\$3)) same (pipeline with stag\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/12/06 12:29
S40	3	((predict\$3 near4 operand\$1) with (availab\$5 or read\$5 or free or result\$3)) same (confidence)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/12/06 13:22
S41	0	(predict\$3 with stage with result with confidence).clm.	US-PGPUB	OR	OFF	2006/12/06 13:22
S42	1	(predict\$3 same stage same result same confidence).clm.	US-PGPUB	OR	OFF	2006/12/06 13:24
S43	1	(predict\$3 same dependent same result same confidence).clm.	US-PGPUB	OR	OFF	2006/12/06 13:24
S44	3	(predict\$3 and stage and result and confidence).clm.	US-PGPUB	OR	OFF	2006/12/06 13:25
S45	5	(predict\$3 and dependent and result and confidence).clm.	US-PGPUB	OR	OFF	2006/12/06 13:33
S46	2	(Jones).in. and (Prime).as.	US-PGPUB; USPAT	OR	OFF	2006/12/06 13:28
S47	0	((Ellis-R).xa. or (Ellis-R).xp. or (Donohue-L).xa. or (Donohue-L).xp.) and (Operand and (predict\$5 or availab\$5)).ti.	US-PGPUB	OR	OFF	2006/12/06 13:36
S48	0	((Ellis-R).xa. or (Ellis-R).xp. or (Donohue-L).xa. or (Donohue-L).xp.) and (Operand and (predict\$5 or availab\$5))	US-PGPUB	OR	OFF	2006/12/06 13:35
S49	0	((Ellis-R).xa. or (Ellis-R)".xp") and (Operand and (predict\$5 or availab\$5))	US-PGPUB	OR	OFF	2006/12/06 13:35

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S50	0	((Ellis-R).xa. or (Ellis-R).xp.) and (Operand and (predict\$5 or availab\$5))	US-PGPUB	OR	OFF	2006/12/06 13:35
S51	18	(Ellis) and (Operand and (predict\$5 or availab\$5))	US-PGPUB	OR	OFF	2006/12/06 13:36
S52	405	(Ellis) and (Operand and (predict\$5 or availab\$5))	US-PGPUB; USPAT	OR	OFF	2006/12/06 13:36
S53	0	((Ellis-R).xa. or (Ellis-R).xp. or (Donohue-L).xa. or (Donohue-L).xp.) and (Operand and (predict\$5 or availab\$5)).ti.	US-PGPUB; USPAT	OR	OFF	2006/12/06 15:30
S54	3	((Ellis\$).xa. or (Ellis\$).xp. or (Donaghue\$).xa. or (Donaghue\$).xp.) and (predict\$3 and future and availab\$5).ti.	US-PGPUB; USPAT	OR	OFF	2006/12/06 15:43
S55	85	((Ellis\$).xa. or (Ellis\$).xp.) and (operand\$1 with availab\$5)	US-PGPUB; USPAT	OR	OFF	2006/12/06 15:44
S56	73	((Ellis-R\$).xa. or (Ellis-R\$).xp.) and (operand\$1 with availab\$5)	US-PGPUB; USPAT	OR	OFF	2006/12/06 15:44
S57	73	((Ellis-Richard\$).xa. or (Ellis-Richard\$).xp.) and (operand\$1 with availab\$5)	US-PGPUB; USPAT	OR	OFF	2006/12/06 15:44
S58	344	(712/214).CCLS.	US-PGPUB; USPAT	OR	OFF	2007/03/21 13:20
S59	519	(712/215).CCLS.	US-PGPUB; USPAT	OR	OFF	2007/03/21 13:20
S60	572	(712/218).CCLS.	US-PGPUB; USPAT	OR	OFF	2007/03/21 13:51
S61	1	("20050060518").PN.	US-PGPUB; USPAT	OR	OFF	2007/03/21 13:51
S62	12503	(confidence\$1 or probabilit\$3 or predict\$3) near4 data near4 (availab\$6 or read\$5 or us\$5)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/21 14:10
S63	3639	(confidence\$1 or probabilit\$3) near4 data near4 (availab\$6 or read\$5 or us\$5)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/21 14:11
S64	271	(confidence\$1 or probabilit\$3) near4 data near4 availab\$6	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/21 14:12
S65	0	((confidence\$1 or probabilit\$3) near4 data near4 availab\$6) with (dependen\$4 near4 instruction\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/21 14:12

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S66	1	(confidence\$1 or probabilit\$3) near4 data near4 availab\$6 near4 dependen\$4	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/21 14:13
S67	5	(confidence\$1 or probabilit\$3) with data with availab\$6 with dependen\$4	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/21 14:16
S68	22	(confidence\$1 or probabilit\$3) same (data with availab\$6 with dependen\$4)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/21 14:22
S69	1	("7145607").PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/21 14:22
S70	1	("7146607").PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/21 14:23
S71	1	("20040194074").PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/21 14:23
S72	33	(multi?thread\$3 or thread\$3) near4 (instruction\$1 near4 issu\$5) near4 (stall\$3 or halt\$3 or bubble\$1 or stop\$5)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/27 13:29
S73	1	(multi?thread\$3 or thread\$3) near4 (instruction\$1 near4 issu\$5) near4 (stall\$3 or halt\$3 or bubble\$1 or stop\$5) near4 (confidence\$1 or probabilit\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/27 13:29
S74	0	((multi?thread\$3 or thread\$3) near4 (simultaneous\$2 or parallel\$1)) with ((instruction\$1 near4 issu\$5) near4 (stall\$3 or halt\$3 or bubble\$1 or stop\$5) near4 (confidence\$1 or probabilit\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/27 13:30
S75	0	((multi?thread\$3 or thread\$3) near4 (simultaneous\$2 or parallel\$1)) same ((instruction\$1 near4 issu\$5) near4 (stall\$3 or halt\$3 or bubble\$1 or stop\$5) near4 (confidence\$1 or probabilit\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/27 13:30

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S76	1	((multi?thread\$3 or thread\$3) near4 (simultaneous\$2 or parallel\$1)) and ((instruction\$1 near4 issu\$5) near4 (stall\$3 or halt\$3 or bubble\$1 or stop\$5) near4 (confidence\$1 or probabilit\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/27 13:30
S77	1	((multi?thread\$3 or thread\$3) near4 (simultaneous\$2 or parallel\$1)) and ((instruction\$1) near4 (stall\$3 or halt\$3 or bubble\$1 or stop\$5) near4 (confidence\$1 or probabilit\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/27 13:30
S78	9	((multi?thread\$3 or thread\$3) near4 (simultaneous\$2 or parallel\$1)) with ((instruction\$1) near4 (stall\$3 or halt\$3 or bubble\$1 or stop\$5))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/27 13:32
S79	0	((multi?thread\$3 or thread\$3) near4 (simultaneous\$2 or parallel\$1)) with ((instruction\$1 near4 issu\$6) near4 (stall\$3 or halt\$3 or bubble\$1 or stop\$5))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/27 13:33
S80	2	((multi?thread\$3 or thread\$3) near4 (simultaneous\$2 or parallel\$1)) same ((instruction\$1 near4 issu\$6) near4 (stall\$3 or halt\$3 or bubble\$1 or stop\$5))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/27 13:34
S81	135	((multi?thread\$3 or thread\$3) near4 (simultaneous\$2 or parallel\$1)) and ((instruction\$1 near4 issu\$6) near4 (stall\$3 or halt\$3 or bubble\$1 or stop\$5))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/27 13:34
S82	3	((multi?thread\$3 or thread\$3) near4 (simultaneous\$2 or parallel\$1)) and ((instruction\$1 near4 issu\$6) near4 (stall\$3 or halt\$3 or bubble\$1 or stop\$5) near4 (data near4 dependen\$5))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/27 13:35
S83	4	((multi?thread\$3 or thread\$3) near4 (simultaneous\$2 or parallel\$1)) and ((instruction\$1 near4 issu\$6) with (stall\$3 or halt\$3 or bubble\$1 or stop\$5) with (data near4 dependen\$5))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/27 13:35
S84	56	((multi?thread\$3 or thread\$3) near4 (simultaneous\$2 or parallel\$1)) and ((instruction\$1 near4 issu\$6) with (data near4 dependen\$5))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/27 13:35
S85	34	((multi?thread\$3 or thread\$3) near4 (simultaneous\$2 or parallel\$1)) and ((instruction\$1 near4 issu\$6) near4 (data near4 dependen\$5))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/27 13:35

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S86	3	((multi?thread\$3 or thread\$3) near4 (simultaneous\$2 or parallel\$1)) same ((instruction\$1 near4 issu\$6) near4 (data near4 dependen\$5))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/27 13:37
S87	5	((multi?thread\$3 or thread\$3) near4 (simultaneous\$2 or parallel\$1)) same ((instruction\$1 near4 issu\$6) with (data near4 dependen\$5))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/27 13:45
S88	25	((multi?thread\$3 or thread\$3)) same ((instruction\$1 near4 issu\$6) with (data near4 dependen\$5))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/27 13:46

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Scholar Results 1 - 9 of 9 for **(multi-threaded or threaded) + "data dependency" + stall + (probability or confidence value)**. (0.16 seconds)

All Results

Tip: Try removing quotes from your search to get more results.

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[Speculative instruction issue in a simultaneously multithreaded processor](#)

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VR Augsburg, JT Bridges, MS McIlvaine, TA ... - 2005 - freepatentsonline.com

[A Paithankar](#)

... an instruction issues, a **data dependency** could cause ... performance in simultaneous, **multi-threaded** microprocessor ... latency for single **threaded** microprocessors can ...

[D Grunwald](#)

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[Selective eager execution on the PolyPath architecture - group of 11 »](#)

A Klauser, A Paithankar, D Grunwald - ACM SIGARCH Computer Architecture News, 1998 - doi.ieeecs.org

... from re- search in branch **confidence** estimation (4 ... which is similar to a **multi-threaded** architecture executing ... areas, such as **dependence** and **value** pre- diction ...

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[VPW1 Program](#)

D Balkan, D Kaeli, J Kalamatianos, R Desikan, S ... - csl.cornell.edu

... **Value** Prediction for Speculative Parallel **Threaded** Computations ... **depen-** dent instructions to **stall**, waiting for ... if the **value** of the **confidence** counter associated ...

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[Data page layouts for relational databases on deep memory hierarchies - group of 16 »](#)

A Ailamaki, DJ DeWitt, MD Hill - The VLDB Journal The International Journal on Very Large ..., 2002 - Springer

... when executing OLTP workloads on **multi- threaded** architectures [24 ... measuring **stall** time: a **stall** cycle may ... functional unit unavailability, **data dependency**, etc ...

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[Systematisch ontwerp van kosteffectieve sprongvoorspelling On the Systematic Design of Cost- ...](#)

V Desmet - elis.ugent.be

... A **data dependency** arises when an instruction needs the result of a preceding instruction, whereas the flow of processed instructions is determined by the ...

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[\[PS\] Executing Multithreaded Programs Efficiently by Robert D. Blumofe Sc. B., BrownUniversity \(1988\) SM, ... - group of 3 »](#)

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... in me or why he was so unwaivering in his **confidence**, but I ... Cilk runtime system automatically manages the low-level details of **thread** schedul- ... Measured **Value**: ...

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... in me or why he was so unwaivering in his **confidence**, but I ... runtime system automatically manages the low-level details of **thread** schedul- ing ... Measured **Value**: ...

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[Slipstream Execution Mode for CMP-based Shared Memory Systems](#)

KZM Ibrahim - 2003 - lib.ncsu.edu

... instructions in the same **thread**. ... **stall** time due to the memory system. ... (lock-free) algorithms [51, 34]. If the collision **probability** is high (for example, same ...

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[Application-Tuned Processor Architectures - group of 9 »](#)

T Sherwood - 2003 - charlotte.ucsd.edu

... 58 3. Speculative **Threaded** Execution 72

2. **Value Prediction Confidence**

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All Results Did you mean: (multithreaded or threaded) + "data dependency" + stall + (probability)

J Dean

Multithreaded processor architectures - group of 4 »

J Hicks

GT Byrd, MA Holliday - Spectrum, IEEE, 1995 - ieeexplore.ieee.org

W Weihl

... Montreal, have aimed at adding multi- threaded features to ... to switch contexts than to stall the processor for ... instructions from the same thread and switching ...

C Waldspurger

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R Blumofe

Speculative instruction issue in a simultaneously multithreaded processor

VR Augsburg, JT Bridges, MS McIlvaine, TA ... - 2005 - freepatentsonline.com

... an instruction issues, a data dependency could cause ... performance in simultaneous, multi-threaded microprocessor ... latency for single threaded microprocessors can ...

Cached - Web Search

CARNEGIE MELLON

JA Miller - ece.cmu.edu

... occurs and the probability of the cache containing ... In traditional fine grained multithreading each thread is allocated ... N is large enough, the stall cycles from ...

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On-line testing and recovery in TMR systems for real-time applications - group of 3 »

SY Yu, EJ McCluskey - Test Conference, 2001. Proceedings. International, 2001 - ieeexplore.ieee.org

... the resource usage and data dependency, and it ... a copy means a replicated computation thread. ... a) Checkpoint_mit ERRO Recovery_mit Stalls operation, invalidates ...

Cited by 8 - Related Articles - Web Search - BL Direct

[CITATION] Space-Efficient Scheduling of Multithreaded Computations - group of 10 »

RD Blumofe, CE Leiserson - SIAM J. Comput., 1998

... task has executed, execution of the consuming thread cannot continue; the thread stalls. Once the producing task executes, the data dependency is resolved ...

Cited by 85 - Related Articles - Web Search - BL Direct

An implementation of scoreboarding mechanism for ARM-based SMT processor - group of 2 »

CY Heo, KB Choi, IP Hong, YS Lee - ASIC, 2003. Proceedings. 5th International Conference on Very ..., 2003 - mpu.yonsei.ac.kr

... issue restriction by true data dependency If the the ... array for a single thread can operate ... high performance figures through the multi-threaded architecture, it ...

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Improving database performance on simultaneous multithreading processors - group of 8 »

J Zhou, J Cieslewicz, KA Ross, M Shah - Proceedings of the 31st international conference on Very ..., 2005 - portal.acm.org

... implement each database operator in a multi-threaded fashion. ... has a high degree of data dependency and future ... cache misses than a single-threaded implementation ...

Cited by 9 - Related Articles - Web Search - BL Direct

[PS] Multithreaded vector architectures - group of 18 »

R Espasa, M Valero - Proceedings of the 3rd International Conference on High ..., 1997 - research.ac.upc.es

... A second main difference with previous multi- threaded superscalar work ... until it blocks on some data dependency or some ... When a thread blocks, we chose some other ...

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[PS] Scheduling Algorithms for Strict Multithreaded Computations - group of 4 »

P Fatourou, P Spirakis - Proc. of the 7th Annual International Symposium on ..., 1996 - citi.gr

... edges of the DAG are spawn, continue and data dependency edges. ... 3. If a thread A stalls or dies, its processor ... of thread A i in the activation tree (at most k ...

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Analytic Modeling of Network Processors for Parallel Workload Mapping

N WENG, T WOLF - ecs.umass.edu

... all. In addition, all actual load and store addresses are available, which can be used for an accurate data dependency analysis. ...

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[CITATION] Space-Efficient Scheduling of Multithreaded Computations - group of 10 »

RD Blumofe, CE Leiserson - SIAM J. Comput., 1998

... Once the producing task executes, the data dependency is resolved, which enables the ... thread Γ_1 . At step 6, thread Γ_1 is ready ... A multithreaded computation ...

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RD Blumofe, CE Leiserson - Journal of the ACM (JACM), 1999 - portal.acm.org

... Phrases: Critical-path length, Multithreading, Multiprocessor, Random-ized algorithm, Thread scheduling, Work ... well-structured) multithreaded computations ...

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P Fatourou, P Spirakis - Proc. of the 7th Annual International Symposium on ..., 1996 - citi.gr

... of the DAG are spawn, continue and data dependency edges ... for the execution of a k-strict multithreaded computation with ... Since for $i = 1; 2; \dots; n$, thread A_i ...

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B Sinharoy - The Computer Journal, 1997 - Br Computer Soc

... Multithreading can significantly reduce the communication and ... this paper as multithreaded multicomputers (MTMCs) ... for special thread synchronization instructions ...

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Dynamic scheduling in RISC architectures - group of 12 »

A Bolychevsky, CR Jesshope, VB Muchnick - Computers and Digital Techniques, IEE Proceedings-, 1996 - ieeexplore.ieee.org

... The alternative approach is multithreading, which is far from ... good performance from a multithreaded processor; also ... In threaded code, if both operands of a ...

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R Ronen, A Mendelson, K Lai, SL Lu, F Pollack, JP ... - Proceedings of the IEEE, 2001 - ieeexplore.ieee.org

... to go beyond optimizing single-thread performance (latency ... 1) Simultaneous Multithreading: Even with super ... coming from either multithreaded parallel programs ...

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GT Byrd, MA Holliday - Spectrum, IEEE, 1995 - ieeexplore.ieee.org

... Scientific Computer (ASC) used multithreading techniques ... aimed at adding multithreaded features to ... In contrast, multithreaded processor architectures ...

Cited by 49 - Related Articles - Web Search - BL Direct

Speculative instruction issue in a simultaneously multithreaded processor

VR Augsburg, JT Bridges, MS McIlvaine, TA ... - 2005 - freepatentsonline.com

... increase performance in simultaneous, multi-threaded microprocessor ... achieves the maximum multithreaded throughput ... latency for single threaded microprocessors can ...

Cached - Web Search

CARNEGIE MELLON

JA Miller - ece.cmu.edu

... ulator, Simultaneous Multithreaded Adjustable Cache Simulator (SMACS) was developed. ... In traditional fine grained multithreading each thread is allocated ...

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D ROBERT, E CHARLES - cs.utexas.edu

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